

## **AMENDMENTS**

### **Amendments to the Specification:**

Please amend paragraph 0050 as follows:

The CPUs 606, 614 are configured, in one embodiment, to process operational commands within the source 602 and target 604, respectively. The electronic memory devices 608, 616 are configured, in one embodiment, to store data and metadata that may be related to the operation of the source 602 and target 604, respectively. In one embodiment, the electronic memory device may comprise a computer readable storage medium. The MRB generation module 612 is configured, in one embodiment, to generate a microcode reconstruct and boot (MRB) data structure. The MRB structure will be discussed further with reference to Figures 7-10. The MRB processing module 620 is configured, in one embodiment, to process a MRB data structure stored on the target 604. In one embodiment, processing a MRB data structure may include simply storing the data in the memory 616.